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MEMC 00-1400(2806.1) PATENT

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HE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Robert J. Falster et al.

Art Unit 1765

Serial No. 10/039,196 Filed January 2, 2002

Confirmation No. 7221

For PROCESS FOR PREPARING SINGLE CRYSTAL SILICON HAVING IMPROVED GATE OXIDE INTEGRITY

April 8, 2002

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS,

SIR:

INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. 1.97 and 1.98 and MPEP 609, and in compliance with the duty of disclosure set forth in 37 C.F.R. 1.56, applicants submit copies of the references listed on the attached PTO/SB/08A for consideration by the Patent and Trademark Office in the above-entitled application and to be made of record therein.

Respectfully submitted,

Derick E. Allen, Reg. No. 43,468

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PTO/SB/08A Complete if Known		te if Known
INFORMATION DISCLOSURE	Application Number	10/039,196
STATEMENT BY APPLICANT	Filing Date	January 2, 2002
(use as many sheets pspecessary)	Confirmation Number	7221
(a)	First Named Inventor	Robert J. Falster
(APR O 8 2002 A	Group Art Unit	1765
THE	Examiner Name	
Sheet 1 of 2	Attorney Docket No.	MEMC 00-1400 (2806.1)

		U.S. Patent Document				
Examiner Initials*	Cite No.1	Number	Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	
	1	5,919,302		Falster et al.	07-06-1999	
	2	5,994,761		Falster et al.	11-30-1999	
	3	6,180,220		Falster et al.	01-30-2001	
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_	5	6,254,672		Falster et al.	07-03-2001	-
	6	6,287,380		Falster et al.	09-11-2001	
	7	6,336,968		Falster	01-08-2002	
Examiner Initials*	Cite No.	Include name of the autitem (book, magazi	thor (in CAPITA ne, journal, seri	T LITERATURE DOCUMEN L LETTERS), title of the article (when ial, symposium, catalog, etc.) date, pa her, city and/or country where publisher.	appropriate), title of the ge(s), volume-issue	Te
	8	R. FALSTER "The Phenomenology of Dielectric Breakdown in Thin Silicon Dioxide Films: Al Cathodes and p-type Si Anodes" J. Appl. Phys., Vol. 66, No. 7 (1989) pp. 3355-3370.			oxide Films: Al	
R. FALSTER et al. "On the Properties of the Intrinsic Point Defects in Silicon: A Perspective f Crystal Growth and Wafer Processing" Phys. Stat. Sol. (b), Vol. 222, No. 1 (2000) pp. 219-24			A Perspective from 000) pp. 219-244.			

			
Examiner		Date	
Signature		Considered	

^{*}EXAMINER initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered include copy of this form with next communication to applicant.

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Tunique citation designation number: See attached kinds of U.S. Patent Documents: Enter Office that issued the document, by the two-letter code (W.P.C. Standard ST.3): For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the senial number of the patent document. I kind of document by the appropriate symbols as indicated on the document under AIPO Standard ST. 16 if possible: Applicant is to piace a check mark here if English language Translation is attached or place an "A" here if English language abstract is attached.

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APR 0 8 2002 2	Examiner Name		
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		OTHER ART - NON PATENT LITERATURE DOCUMENTS
	10	T. SINNO et al. "Point Defect Dynamics and the Oxidation-Induced Stacking-Fault Ring in Czochralski-Grown Silicon Crystals" J. Electrochem. Soc., Vol. 145, No. 1 (1998) pp. 302-318.
•	11	A. VIRZI "Computer Modelling of Heat Transfer in Czochralski Silicon Crystal Growth" J. of Crystal Growth, Vol. 112, No. 4 (1991) pp. 699-722.
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